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In re Patent Application of:

Mario SAGGIO, et al.

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For: SILICON IMPROVED SCHOTTKY
BARRIER DIODE

Examiner:

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant hereby submits for filing under 37 CFR 1.97 a disclosure statement. In submitting these references, no representation is made or implied that the references are or are not material to the examination of this application. The patents, publications or other information of which Applicant is presently aware are listed in Form PTO/SB/08A submitted herewith. Copies of all such patents and publications are enclosed.

No fee is believed due. However, any fee deficiency associated with this submittal may be charged to Deposit Account No. 50-1123.

Respectfully submitted,

April 2, 2002

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